

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 11-243039

(43)Date of publication of application : 07.09.1999

(51)Int.Cl. H01L 21/02

H01L 27/12

(21)Application number : 10-365132

(71)Applicant : CANON INC

(22)Date of filing : 22.12.1998

(72)Inventor : SAKAGUCHI KIYOBUMI
YONEHARA TAKAO

(30)Priority

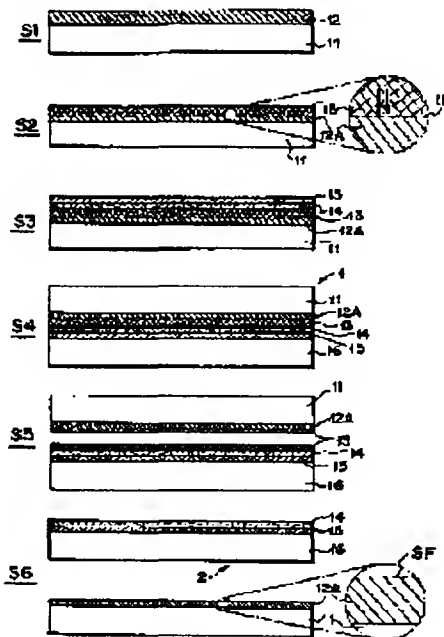
Priority number : 09361091 Priority date : 26.12.1997 Priority country : JP

(54) MANUFACTURE OF SEMICONDUCTOR MEMBER AND SEMICONDUCTOR MEMBER

(57)Abstract:

PROBLEM TO BE SOLVED: To provide an inexpensive method for manufacturing a semiconductor member, in which the irregularities of a boundary face between a porous layer and a substrate can be reduced, and the reproducibility of a separating position can be increased.

SOLUTION: This method includes a process for forming an additional layer 12, to which an element for controlling a conductive type is added on at least one surface side of a semiconductor substrate 11, making porous the surface of the additional layer 12 for obtaining a porous layer 13 which is smaller in thickness than that of the additional layer 12, and forming a non-porous layer 14 on the porous layer 13 for preparing a first substrate, a process for adhering the first substrate to a second substrate 16 for obtaining a multilayer structure in which the non-porous layer 14 is positioned inside, and a process for separating the multi-layer structure from the porous layer 13.



LEGAL STATUS

[Date of request for examination] 05.07.1999

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3013932

[Date of registration] 17.12.1999

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office